

# Preacc Reference Source

*Local application*

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A method for monitoring certain digital status in the Preaccelerator can help to avoid having the reference source ON if the anode supply is OFF. This note describes the function of an LA called HVRS that implements this monitoring.

Every 15 Hz cycle, the two status bits are checked for the special condition above. If the reference source is ON, and the anode supply is OFF, and it has been OFF for a specified number of cycles, then issue the required 3-cycle hi-going pulse to turn off the reference source. After one second, resume scanning for the special condition.

## *Parameter layout*

Here are the parameters needed for performing this logic.

<i>Prompt</i>		<i>Size</i>	<i>Meaning</i>
ENABLE	B	2	Usual LA enable Bit#
REF SRC	B	2	Reference source status Bit#, where 1=ON
ANODE	B	2	Anode power supply status Bit#, where 1=ON
REF OFF	B	2	Reference source OFF control Bit#. Pulse hi for 3 cycles.
STAT CY		2	#cycles of anode power supply OFF status before taking action

## *Internal Log*

An internal log of the actions required to turn off the reference supply is maintained. The only diagnostic data is the 8-byte BCD time-of-day when the action was taken.

## *Details*

The LA monitors the two status bits via calls to BitRaw. The log has room for 60 entries.