

New IRM Memory Allocation

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Token ring is no longer supported by IRM code. This eliminated need for the 64K token ring receive buffer at 0x60000. And it allowed expansion of the Region I dynamic memory from 256K based at 0x20000 to 320K.

The former 128K of ethernet receive buffers at 0x160000 was moved to 0x280000, allowing for 512K bytes, which permits 340 full-size ethernet frames. Since an ethernet buffer location is committed to the hardware ahead of time, even small ethernet frames require a full-size buffer.

If we can move the arcnet receive buffer space, which is 64K based at 0x70000, then we can expand Region I memory by another 64K, for a total of 384K. The token ring transmit buffers are located in the 64K based at 0x80000. The arcnet transmit buffers use 64K based at 0x90000. Since the ethernet receive buffers have now been moved to the 512K based at 0x280000, thus freeing up the 128K from 0x160000-0x17FFFF, the arcnet buffers can be moved to the old ethernet receive buffer area. This would free up the space from 0x70000 to 0x9FFFF, which could allow 512K for Region I memory.

